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THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/797,664	
				Filing Date	March 11, 2004	
				First Named Inventor	Pieter Van GROOS	
				Art Unit	2851	
(Use as many sheets as necessary)			s necessary)	Examiner Name	KIM, Peter B.	
Sheet	1	of	1	Attorney Docket Number	1857.7540002	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
	NPL1	Office Action for Chinese Application No. 2004100326869 issued April 4, 2008, 12 pgs.	
	NPL2	Peter van Zant, Microchip Fabrication: A Practical Guide to Semiconductor Processing, Third Edition, McGraw Hill Publishing Co., 1997 (the whole book).	
	NPL3		
	NPL4		
	NPL5	·	
	NPL6		
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